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USSN 10/678,498

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:
Yang Chong, *et al.*

Serial No.: 10/678,498

Filed: October 3, 2003

For: *Supply Voltage Detection Circuit*

Confirmation No.: 3364

Art Unit: 2819

Examiner: Don P Le

Attorney Docket No.: 060889-0105-US
(formerly 9818-
105-999)

RESPONSE

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This responds to the Office Action mailed May 17, 2004.

In the office action, the Examiner rejected claims 38-64 on the basis of double patenting with respect to claims 1-37 of U.S. Patent No. 6,630,844.

The terminal disclaimer submitted herewith removes the double patenting rejection.

Please charge the terminal disclaimer fee of \$110.00 to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310. A duplicate copy of this page is enclosed.

In view of the foregoing it is respectfully submitted that this application is in condition for allowance. Such action is respectfully requested.

Please direct any inquiry to Frank E. Morris at (212) 309-6632.

Dated August 17, 2004

Respectfully submitted,
Morgan, Lewis & Bockius LLP


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